Characterization of strain fields around Through-Silicon Vias by second-harmonic scanning microscopy

Ming Lei, Farbod Shafiei and M. C. Downer

Epi-Optics Laboratory, Department of Physics, The University of Texas at Austin, Austin TX USA

www.ph.utexas.edu/~femtosec/

3D integration is a forefront technique for achieving high transistor density while reducing inter-chip RC delay and power consumption. "Through-Silicon Vias" (TSVs) achieve 3D integration by interconnecting vertically stacked devices. These short vertical interconnects enable better electrical performance and consume less power than longer interconnects needed in 2D integration. Cu is widely used as the TSV material because it is compatible with back-end-of-line processes, and has favorable electrical and mechanical properties. However, the large mismatch in coefficients of thermal expansion between Cu and Si induces thermal stresses during fabrication, testing and operation of TSV structures that can induce defects that degrade carrier mobility within the strain field, voids within the metal interconnect, and cracking of the Si wafer. A strong need exists for fast non-invasive methods of characterizing strain fields surrounding TSVs. Here we show that scanning SHG microscopy is sensitive to these strain fields. Even though SHG is forbidden to lowest order from unstrained bulk Si, strain gradients break the centrosymmetry of the diamond-structure lattice, creating a second-order dipolar optical nonlinearity.



AT AUSTIN